



Attorney Docket No. 38105.830023.US1  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Kadambi, Govind R.

Serial No. 10/605,593

Filed: October 10, 2003

For: OPTIMUM UTILIZATION OF SLOT GAP  
IN PIFA DESIGN

Examiner: Cao, Huedung X.

Art Unit: 2821

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patent  
P.O. Box 1450  
Alexandria, VA 22313

Sir:

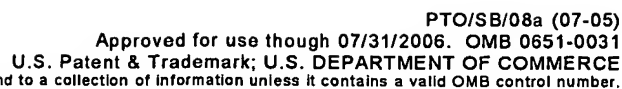
Pursuant to 37 C.F.R. § 1.97 the Examiner may wish to consider the references listed on the attached PTO Form 1449. In submitting these references for the Examiner's consideration, no representation is made or implied that these references are or are not material to the examination of the application. The Examiner is encouraged to make his or her own determination of materiality. Copies of the references are provided herewith.

This Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97 (c), after issuance of a first office action in the case. However, the undersigned hereby certifies that each item of information contained in the attached Form 1449 was not known more than three months prior to the filing of this IDS. Thus, no petition fee is believed required. However, any fees associated with this communication may be charged to Deposit Account No. 08-2623.

Signed at Denver, Colorado, this March 10, 2006.

Respectfully submitted,

Brian Kinnear, Reg. No. 43,717  
Holland & Hart LLP  
555 17th Street, Suite 3200  
Denver, Colorado 80202-3979  
Telephone: (303) 295-8170  
Fax: (303) 295-8261



## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

**COMPLETE IF KNOWN**

Application Number	10/605,593
Filing Date	October 10, 2003
First Named Inventor	Kadambi, Govind R.
Art Unit	2821
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Attorney Docket Number	38105.830023.US1

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## U.S. PATENT DOCUMENTS

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**FOREIGN PATENT DOCUMENT**

Examiner Initials*	Cite No.*	Foreign Patent Document Country Code *Number 4 Kind Code 5(if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T6
		EP A 1 241 733	09/18/2002	Edimo, Marc		
		EP A 1 128 466	08/29/2001	Filtronic LK Oy		
		WO 02/43182 A	05/30/2002	Siemens		
		WO 02/075853 A	09/26/2002	Matsushita Electronic Industrial Co.		

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designations number (optional). 2 See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. 3 Enter Office that issued the document by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. this collection is estimated to take 2 hours to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: COMMISSIONER FOR PATENTS, P.O. Box 1450, Alexandria, VA 22313-1450.**

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